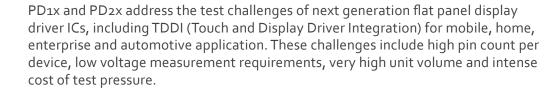


Diamond_x PD_{1x}/PD_{2x}

Test Solution for Ultra-High Definition Display Driver ICs







- 320 channels per board
- >5000 channels per system
- Digitizer per pin
- Continuity / leakage measure unit per pin
- Precision voltage references
- Multi-threaded multi-lane PCI Express data transfer and processing
- Direct probe and COF handler interfaces



Automotive



Consume



Flat Panel Display



IoT/IoV & Optoelectronics



Industrial & Medical



MCU



Mobility

Highlights

- Integrated display drivers for mobile and tablet applications, including touch and display driver integration (TDDI)
- Large panel television and monitor applications, including ultra-high definition and 240 Hz refresh rate
- Extended range selection
- 128k capture memory per channel
- Industrial and automotive display drivers
- 320 Channels per Instrument
- 16-bit 123 ksps Digitizer

- Max Current Constant/Pulsed ± 10.24V, 0/+20V
- Max Pins 6,080 (20 slot), 12,160 (40 slot)



Diamond_x PD_{1x}/PD_{2x}

Test Solution for Ultra-High Definition Display Driver ICs

 $PD1_{x}$ and $PD2_{x}$ address the test challenges of the full spectrum of next generation display driver devices, from TDDI (Touch and Display Driver Integration) devices for mobile and transportation products to the latest Ultra-HD televisions.

These challenges include high pin count per device, extreme low leakage, high voltage accuracy requirements, and very high unit volume and cost of test pressure.

PD1 $_{\rm X}$ and PD2 $_{\rm X}$ address these challenges by offering 320 channels of per pin digitizers providing 1 mV accuracy, optimized continuity and leakage measurement, and leading edge data transport.

Digitizer input bandwidth is optimized to support stringent slew rate testing of the latest high-refresh rate Ultra-HD display drivers. With 1 Gohm impedance, $PD2_x$ can easily test leakage to single digit nanoamp accuracy to ensure oppm test quality.

The revolutionary channel density of PD1 $_{\rm X}$ and PD2 $_{\rm X}$ enable over 5000 channels to be configured in a system for multi-site test strategies that exceed the throughput and cost effectiveness of any existing solution.

The Diamondx architecture is uniquely designed to maximize throughput and minimize test costs, and PD_{1x} and PD_{2x} take full advantage of these architectural capabilities.

The addition of the PD1 $_{\rm X}$ and PD2 $_{\rm X}$ to the comprehensive suite of Diamondx analog, digital and wireless instruments provides a compelling solution for display driver and TDDI devices, in addition to the full MCU, SoC/ASSP, Automotive and wireless test capability of the Diamondx.

PD₁×

- 320 channels per instrument
- 16 bit, +/-1 mV accuracy
- 123 ksps sampling rate (320 channels)
- 243 ksps sampling rate (160 channels)
- 128 k sample memory per channel
- Real-time sample averaging
- 500 $M\Omega$ input impedence
- -10 V to +10 V voltage range
- +/-10.5 V reference voltage source
- Synchronous sampling

PD₂

- 320 channels per instrument
- 16 bit, +/-1 mV accuracy
- 123 ksps sampling rate (320 channels)
- 243 ksps sampling rate (160 channels)
- 128 k sample memory per channel
- Real-time sample averaging
- 1 G Ω input impedance
- $\bullet\,$ -10 V to +10 V and o to +20 V voltage ranges
- -10.5 V to +20 V reference voltage sources
- Synchronous sampling

All specifications are subject to change without notification and are for reference only. For detailed performance specifications, please contact Cohu.